

# OLIVER GARROW

CTO / COO / ENGINEERING VP

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## OBJECTIVE

Founder or C-level position in new startup or small corp focused on product engineering & testing, transfer to volume production. Prefer to work in Aerospace through ready to work in industrial, automotive, maritime industries.

Uber-engineer thrilled to manage engineering organizations, creating innovative and novel solutions, using lightweight composites, CNC machining and 3D printing process, latest software, AI, graphics and embedded systems.

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## EXPERIENCE

### AEROSPACE

2004 - 2024



**Serial entrepreneur** who founded **3 aerospace startups** (Garrow Air, Elytron and Sabrewing Aircraft), helped raise close to **\$10M**. Invented and designed **5 generations of VTOL aircraft**, manned and unmanned, addressing regional transport, air taxi, cargo transport and surveillance, with designs named "Verticopter", "Converticopter", "Elytron", "Elytair", "Rhaegal-A", "Rhaegal-B" and "Saburtron", each one going through full CAD engineering, **FEA/CFD/FLT simulations** and flight tests. Build and tested two full-size airframes (>2,000 lbs. / 500kW+) received **FAA airworthiness** certification, went through ground testing and limited flight test. Completed **UAV wind tunnel testing at NASA AMES**. Designed full ground control stations with telemetry and advanced avionics.

### ELECTRONICS / EMBEDDED SYSTEMS

1993 - 2013

Supported latest electronic chipset launch in many industries including **MCU, CPU, DSP, FPGAs**, worked in most positions from IC Design engineer, to application engineering director, at firms such as **STMicro, Siemens, Nazomi, Infineon, Xilinx, Renesas**. Supported numerous software stacks, OS, DSP, embedded.

### PATENTS

Patent # US 9,499,266 : "Five-Wing Aircraft to Permit Smooth Transitions Between Vertical and Horizontal Flight"  
Patent # US D724,001 S : "Joined-Wing Tilt-Wing Aircraft"  
Patent # US D665,333 S1 : "VTOL Aircraft"  
Patent # US 7,753,309 B2 : "VTOL/STOL Tilt-Prop Flying Wing"  
Patent # US D626,056 S : "AIRCRAFT"  
Patent # US 6,425,101 B1 : "Programmable JTAG Architecture to Support Proprietary Debug Protocol"  
Patent # US 6,769,035 : "Same Single Board Computer System Operable as a System Master and a Bus Target"  
Patent # US 8,219,843 B2 : "Circuit For And Method Of Receiving Video Data", (project manager)

## CONTACT

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## ABOUT ME

I am passionate about inventing, designing, drafting, testing new hardware and transferring to volume production. Will gather best minds for the best output. AMA, EAA, VFS member, trilingual.

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## EDUCATION

**SolidWorks Flow Simulation Training**  
Hawk Ridge Systems, Oakland, CA  
**SolidWorks CAD Essentials and Advanced training**  
Hawk Ridge Systems

**CFD** seminars from various vendors  
**Composites and Fabric Covering Workshops**  
EAA SportAir

**Aircraft Design** using Finite Element Methods  
Martin Hollmann

**Research Thesis, Transputer Parallel Computing**, University of York, UK

**MS Computer Sciences, IC design, embedded software**  
ENSEA Uni.

**BSEE digital/analog electronics, Automation, Power.**  
P.Sabatier University, Toulouse, France

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## SKILLS

CAD, FEA, CFD, MCU, ARM, CPU, GPU, DSP, FPGA, ECU, CNC, CAM, 3DP, UAV, X-plane,